

The inverter can be produced with how many NAND gates?

1

If both inputs of a 2 input NOR gate are connected, the gate will function as an

_____ inverter

A gate can drive a number of load gate inputs up to its specified _____ fan out

One positive pulse with $t_w = 75 \text{ } \mu\text{s}$ is applied to one of the inputs of an exclusive-OR circuit. A second positive pulse with $t_w = 15 \text{ } \mu\text{s}$ is applied to the other input beginning $20 \text{ } \mu\text{s}$ after the leading edge of the first pulse. Which statement describes the output in relation to the inputs?

The exclusive-OR output is a 20 s pulse followed by a 15 s pulse, followed by a 40 s pulse.

A 4 variable AND OR circuit produces a 0 at its Y output. Which combination of inputs is correct?

A = 1, B = 0, C = 1, D = 0

Using the universal property of a NAND gate, one or more NAND gates can be used to replace an _____ any of the options

How many 2 input NOR gates does it take to produce a 2 input NAND gate?

3

A node is defined as _____ a common point in a circuit

Assume that you have a 3 input NAND gate but need only a 2 input gate. The unused input should be _____ connected to a HIGH

The output of an exclusive NOR gate is 1. Which input combination is correct?

A = 0, B = 0

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