

CIT246

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1. AND gate is an electronic circuit that gives a high output (1) only if _____.

all its inputs are low.

One of its inputs is low.

One of its inputs is high.

--->> all its inputs are high.

2. The mathematical Logic used for designing and analysing digital circuits is called _____

arithmetic

--->> boolean algebra

abstract algebra

numerical analysis

3. _____ causes the CPU to temporarily transfer its control from a currently executing program to a different program

exemption

control transfer

--->> Interrupt

priority order

4. The Exclusive-OR of variables $A = 1$ and $B = 0$ equals _____

--->> 1

zero

11

double zero

5. In what ideal order are multiple interrupts being handled in computer system?

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Parallel

Distributed

Relative priority

6. The NAND and NOR gates are called _____

Universal set

Complement Function

--->> Universal Functions

Union set

7. Product of sums form can be implemented using _____

NAND gates

ENOR gates

NOT gates

--->> NOR gates

8. Find the even bit for 1111111, 7-bit binary data?

10

100

101

--->> 1

9. Sum of products form can be implemented using _____ gates.

NOR gates

--->> NAND gates

ENOR gates

NOT gates

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a NOT gate or AND gate

a NOT gate followed by an AND gate

--->> an AND gate followed by a NOT gate

an AND gate followed by a NOR gate

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