CIT211
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1. With paging there is no _____ fragmentation.

      internal

   --->> external

      either type of

      none of the options

2. Sometimes the overhead of keeping track of a hole might be :

      larger than the memory

   --->> larger than the hole itself

      very small

      all of the options

3. Illegal addresses are trapped using the _____ bit.

      error

      protection

   --->> valid Ã¢â,¬â€œ invalid

      access

4. Memory protection in a paged environment is accomplished by:

      protection algorithm with each page

      restricted access rights to users

      restriction on page visibility

   --->> protection bit with each page

5. External fragmentation will not occur when :

      first fit is used

worst fit is used

--->> no matter which algorithm is used, it will always occur

6. For every process there is a _____

--->> page table

copy of page table

pointer to page table

all of the options

7. When there is a large logical address space, the best way of paging would be:

not to page

--->> a two level paging algorithm

the page table itself

all of the options

8. For larger page tables, they are kept in main memory and a _____ points to the page table.

--->> page table base register

page table base pointer

page table register pointer

page table base

9. When the valid Ã¢â‚¬â€œ invalid bit is set to valid, it means that the associated page:

is in the TLB

has data in it

--->> is in the processÃ¢â‚¬â„¢s logical address space

is the systemÃ¢â‚¬â„¢s physical address space

10. The page table registers should be built with _____

--->> very high speed logic

a large memory space

none of the options