

HIGH, LOW, float

[PHY405] The main difference between a register and a counter is
a register has no specific sequence of states

[PHY405] How many methods of shifting data are available?
2

[PHY405] There are how many types of registers
4

[PHY405] Hexadecimal letters A through F are used for decimal equivalent values from:
10 through 15

[PHY405] Assume that a 4-bit serial in/serial out shift register is initially clear. We wish
to store the nibble 1100. What will be the 4-bit pattern after the second clock pulse?
(Right-most bit first)
Zero

[PHY405] The octal numbering system:
simplifies tasks and saves time

[PHY405] An asynchronous binary up counter, made from a series of leading edge-
triggered flip-flops, can be changed to a down counter by
All of the Mentioned

[PHY405] Synchronous construction reduces the delay time of a counter to the delay of:
a single flip-flop and a gate

[PHY405] A register that is capable of shifting the stored word toward the left or right is
called
shift register

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