

Decreases

[PHY310] The phase difference between the input and output voltages of a transistor connected in common emitter arrangement is
180o

[PHY310] The voltage gain in a transistor connected in ___ arrangement is the highest
Common emitter

[PHY310] The most commonly used transistor arrangement is ___ arrangement
Common emitter

[PHY310] The power gain in a transistor connected in ___ arrangement is the highest
Common emitter

[PHY310] The voltage gain of a transistor connected in common collector arrangement is ___
Less than 1

[PHY310] The output impedance of a transistor connected in ___ arrangement is the highest
Common base

[PHY310] The input impedance of a transistor connected in ___ arrangement is the highest
Common collector

[PHY310] The phase difference between the input and output voltages of a transistor connected in common collector arrangement is ___
ZEROo

[PHY310] The phase difference between the input and output voltages in a common base arrangement is
ZEROo

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