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1. In a binary system a positive number has a sign bit of $\qquad$ , while the negative number has a sign bit $\qquad$ .

0,1
2. In what ideal order are multiple interupts being handled in computer system?

Sequential
3. What is overflow?
when the sum of two occupies $n+1$ digit.
4. $\qquad$ causes the CPU to temporarily transfer its control from a currently executing program to a different program

Interupt
5. Find $1 \tilde{A} \cdot s$ and $2 \tilde{A} \cdot s$ complement of 1010 ?
$0101 / 0110$
6. Which CPU register keeps track of the instruction that is to be fetched next?
program counter
7. In a Computer $\qquad$ method, the decimal position is assumed either at the beginning or at the end of a number.

Fixed Point representation
8. The instruction cycle consists of the $\qquad$
fetch cycle and the execution cycle
9. $\qquad$ provides a very simple way for adding and subtracting two numbers in many computers.

Signed 2Ã•s complement
10. Find the even bit for 1111111,7 -bit binary data?

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